

Execution Ethernet Interfaces CANopen Master

1. Standard Ethernet TCP/IP interface with CANopen Master

Typedesignation: BM4-O-ETH-02-00-00

Part No.:365815

Function: Ethernet TCP/IP interface with CANopen Master

Presuppositions: BM44XX-XX-XXXXX-XX and BM4-O-PLC-XX-XX-XX

Information of nameplate:

BM4-O-ETH-02 - 00 - 00 - yyy - zzz

LP 3.0107.02.Xxx

D2000: 6.1302.xxxx

D4020: 6.1307.xxxx

D6030: 6.1303.xxxx

From zzz = 005 with

Ethernet-cross

communication

Firmware Mach SW FPGA-SW

LP- Version	yyy	zzz	Firmware	Mach SW	FPGA-SW
3.0107.02			6.1307	6.1303.	6.1302
B01	000	000	0101	0201	0101
B02	001	000	0101	0201	0101
B03	002	000	0101	0201	0101
B03	002	001	0106	0201	0101
B03	002	002	0107	0201	0101
B03	002	003	0110	0201	0106
B04	003	003	0110	0201	0106
B04	003	004	0113	0201	0108
B04	003	005	0113	0201	0108
B05	004	005	0113	0201	0108
B06	104	005	0113	0201	0108
C02	005	005	0113	0201	0108
C03	006	005	0113	0201	0108
C03	006	006	0121	0201	0110
C04	106	006	0121	0201	0110
D01	007	005	0121	0201	0108
D01	007	006	0121	0201	0110
D02	008	006	0121	0201	0110

Hardware history

The printed circuit board 3.0107.02.B01 is the first official version.

The printed circuit board 3.0107.02.B02 is introduced with AT161_03 from March 31, 2003.

- Adaptation of the parts list to the series production

The printed circuit board 3.0107.02.B03 is introduced with AT441_03 from October 01, 2003.

- Internal change of the plug (solder instead of press)

The printed circuit board 3.0107.02.B04 is introduced with AT076_05 from April 04, 2005.

- Optimized boot behavior (Reset level at the option modules)

The printed circuit board 3.0107.02.B05 is introduced with AT434_05 from December 16, 2005.

- Optimized for internal test equipment (additional resistors)

The printed circuit board 3.0107.02.C02 is introduced with AT434_05 from December 16, 2005.

- Optimized for the procedure of production

The printed circuit board 3.0107.02.C03 is introduced with AT196_06 from June 27, 2006.

- Optimized assembly

The printed circuit board 3.0107.02.D01 is introduced with AT196_06 from June 27, 2006.

- Optimized Layout

The printed circuit board 3.0107.02.D02 / C04/ B06 is introduced with AT215_07 from July 24, 2007.

- Correction for error pattern ‚Prozessor Abort During Data Access‘

The changes are for the software uncritically. New software also runs on old hardware version.

FPGA software history

The FPGA-software 1.02 is the first official version.

The FPGA-software 1.06 is introduced with AT076_05 from November 17, 2003.

- Synchronization behavior improves with restart of the synchronization
- Improvement of the BACI-Timing

The FPGA-software 1.08 is introduced with AT194_05 from June 17, 2005.

- Improvement of the signal quality (influence of the temperature)

The FPGA-software 1.10 is introduced with AT073_07 from March 2, 2007.

- Standardization of software

Firmware history

The Firmware software 1.01 is the first official version.

The Firmware-software 1.06 is introduced with AT489_03 from the November 14, 2003.

- Extension of the functionality on Multi Axis Motion Control

The Firmware-software 1.07 is introduced with AT111_04 from the February 26, 2004.

- Adaptation of the recognition of new flash types

The Firmware-software 1.10 is introduced with AT076_05 from April 04, 2005.

- Synchronization behavior improves with restart of the synchronization
- Improvement of the BACI-Timing

The Firmware-software 1.13 is introduced with AT367_04 from June 22, 2005.

- Qualities of all Firmware-versions (standard and cross communication) combines on a single firmware

The Firmware-software 01.21 is introduced with AT073_07 from March 02, 2007.

- CANopen configurable with ProMaster
- Support of MotionControl
- CANopen Automatically net work management
- CANopen synchronization of process communication in IEC for 256RxPDOs and 256TxPDOs
- CANopen communication profile DS301, DSP 302 and application profile DS405

Indication

The modules CAN-04-00-00 and ETH-01-00-00, ETH-02-00-00 are identical software-technical.

The functionality CANopen Master or Ethernet interface or both is fixed by the assembly of the hardware.

If master CANopen is used for applications with PLCopen Single/Multi Axis, the Ethernet interface is not available (-> Application of CAN-04 and ETH-01).

From Firmware 1.06 the basic devices with LC-SW01 or LC-SW03 can be served with Motion Control.

Update-Indication

Firmware and FPGA-SW always together update at last version. An update is possible only in BNE.

2. Standard Ethernet TCP/IP Interface with cross communication and CANopen Master (inactive)

This version is not any longer supplied. With introduction of the software 01.13 as compatible replacement the ETH02-00-00 is only manufactured.

Typedesignation: BM4-O-ETH-02-00-01

Part No.:373308

Function: Ethernet TCP/IP interface with cross communication and CANopen Master

Presuppositions: BM44XX-XX-XXXXX-XX and BM4-O-PLC-XX-XX-XX

Information of nameplate:

BM4-O-ETH-02 - 00 - 01 - yyy - zzz						
LP 3.0107.02.Xxx						
D2000: 6.1302.xxxx						
D4020: 6.1371.xxxx						
D6030: 6.1303.xxxx						
Ethernet with cross communication					Firmware	Mach SW
						FPGA-SW
	LP-Version				6.1371	6.1303.
3.0107.02	B03	000	000	0101	0201	0101
	B03	000	001	0102	0201	0101
	B03	000	002	0103	0201	0106
	B04	001	002	0103	0201	0106
	B04	001	003	0103	0201	0108
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Hardware history

The printed circuit board 3.0107.02.B03 is the first official version.

The printed circuit board 3.0107.02.B04 is introduced with AT076_05 from April 04, 2005.

- Optimized boot behavior (Reset level at the option modules)

FPGA software history

The FPGA-software 1.01 is the first official version.

The FPGA-software 1.06 is introduced with AT076_05 from November 17, 2005.

- Synchronization behavior improves with restart of the synchronization
- Improvement of the BACI-Timing

The FPGA-software 1.08 is introduced with AT194_05 from June 17, 2005.

- Improvement of the signal quality (influence of the temperature)

Firmware history

The Firmware-software 1.01 is the first official version.

The Firmware-software 1.02 is introduced with AT111_04 from February 26, 2004.

- Adaptation of the recognition of new flashtypes

The Firmware-software 1.03 is introduced from May 19, 2005.

- Compatibility of new FPGAs

Indication

The modules ETH-01-00-01, ETH-02-00-01 are identical software-technical.

The functionality Ethernet interface or Ethernet Interface and CANopen Master fixed by the assembly of the hardware.

Applications with PLCopen Single/Multi Axis are not impossible with this card.

Update-Indication

Firmware and FPGA-SW always together update at last version. An update is possible only in BNE.

This version is not any longer supplied. With introduction of the software 01.13 as compatible replacement the ETH02-00-00 is only manufactured.