# **Execution PRO-01**

# 1. Profibus-DP-Slaveinterface for controller in the basic device

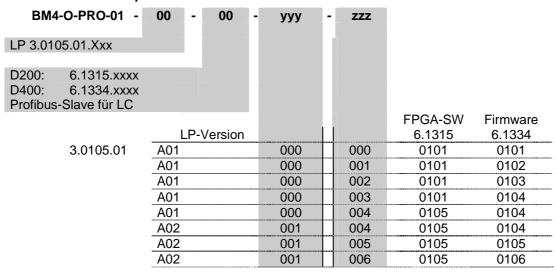
Typedesignation: BM4-O-PRO-01-00-00

Part No.: 350459

Function: Profibus-DP-Slave interface for controller in the basic device

Presuppositions: BM44XX-XX-XXXXXX-XX

#### Information of nameplate:



#### Hardware history

The printed circuit board 3.0105.01.A01 is the first official version.

The printed circuit board 3.0105.01.A02 is introduced with AT076\_05 from March 21, 2005.

Optimized boot behavior (Reset level at the option modules)

#### FPGA software history

The FPGA-software 1.01 is the first official version.

The FPGA-software 1.05 is introduced with AT076\_05 from March 21, 2005.

- Synchronization behavior improves with restart of the synchronization
- Improvement of the BACI-Timing

# Firmware history

The Firmware-software 1.01 is the first official version.

The Firmware-software 1.02 is introduced with AT023\_04 from January 20, 2004.

Adjusted configuration is led correctly at field bus

The Firmware-software 1.03 is introduced with AT093\_04 from February 17, 2004.

LC3 adaptation

The Firmware-software 1.04 is introduced with AT114\_04 from March 03, 2004.

• Extension of functionality for basic equipments with SW03

The Firmware-software 1.05 is introduced with AT101\_05 from April 06, 2005.

- Elimination of errors in the BACI parameters for cyclic communication
- Elimination of the double boot process

The Firmware-software 1.06 is introduced with A321\_06 from October 24, 2006.

- Adjustment to new actual values of b maXX software 3.08
- Correction of internal module recognition

#### Indication

From Firmare 1.04 the basic devices can be served by SW03

# **Update-Indication**

Firmware and FPGA-SW always together update at last version.

# 2. Profibus-DP-Slave interface for controller in the basic device

Typedesignation: BM4-O-PRO-01-00-01

Part No.: 369656

Function: Profibus-DP-Slave interface for PLC01

Presuppositions: BM4-O-PLC-01-XX-XX

# Information of nameplate:



## **Hardware history**

The printed circuit board 3.0105.01.A01 is the first official version.

The printed circuit board 3.0105.01.A02 is introduced with AT076\_05 from th March 21, 2005.

Optimized boot behavior (Reset level at the option modules)

## **FPGA** software history

The FPGA-software 1.01 is the first official version.

The FPGA-software 1.05 is introduced with AT076\_05 from March 21, 2005.

- Synchronization behavior improves with restart of the synchronization
- Improvement of the BACI-Timing

# Firmware history

The Firmware-software 1.01 is the first official version.

The Firmware-software 1.02 is introduced with AT023 04 from January 20, 2004.

• Internal communication corrected with PLC

The Firmware-software 1.03 is introduced with AT093\_04 from February 17, 2004.

Handshake introduced for consistency

# **Update-Indication**

Firmware and FPGA-SW always together update at last version.