

BM4-O-IEI-01

Typedesignation: BM4-O-IEI-01-00-00

Part No.: 366234

Function: Incremental encoder interface

Presuppositions: BM4-O-PLC-01-XX-XX-XX

Information of nameplate:

BM4-O-IEI-01 - 00 - 00 - yyy - zzz						
LP 3.0117.01.Xxx						
D300:	6.1292.xxxx					
D200_1:	6.1301.xxxx					
D200_2:	6.1313.xxxx					
				Firmware Load logic	FPGA	
					BACI-Header in FPGA	
		LP-Version		6.1292	6.1301.	6.1313
3.0117.01		B01	000	000	0101	0101
		B02	001	001	0102	0101
		B03	002	002	0102	0102
		B03	002	003	0102	0105
		B04	003	003	0102	0105
		B04	003	004	0103	0105

Hardware history

The printed circuit board 3.0117.01.B01 is the first official version.

The printed circuit board 3.0117.01.B02 is introduced with AT209_03 from April 24, 2003.

- Signals are prepared at the IEI because of the stability

The printed circuit board 3.0117.01.B03 is introduced with AT487_03 from November 05, 2003.

- Increase of noise immunity of the limit switch input

The printed circuit board 3.0117.01.B04 is introduced with AT076_05 from March 03, 2005.

- Optimized boot behavior (Reset level at the option modules)

Firmware load logic history

The Firmware 1.01 is the first official version.

The Firmware-software 1.02 is introduced with AT209_03 from April 04 ,2003.

- Reset signal is not stable during booting. So preparation at the IEI.

The Firmware-software 1.03 is introduced with AT099_04 from April 04 ,2004.

- Timing/Load behavior FPGA changed

BACI-Header in FPGA

Comment:

BACI-Header in FPGA increases with number, if FPGA, load logic or HW-version changes

FPGA

The software 101 is the first official version.

The software 102 is introduced with AT487_03 from November 05, 2003.

- Signals are prepared at the IEI because of the stability

The software 105 is introduced with AT329_04 from August 02, 2004.

- Improvement BACI-Timing

Update-Indication

Firmware and FPGA-SW always together update at last version. An update is possible only in BNE.